AMENDMENTS TO THE CLAIMS

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.   (Currently Amended) A computer-implemented method for using a processor as a virtual device, said method comprising:

loading data from a first processor to a first location in a common memory in a computer system;

loading software code that processes the data in a second location in the common memory;

writing an instruction block from the first processor to a third location in the common memory, wherein the instruction block includes the first and second locations;

signaling, from a first processor, a second processor from a plurality processors in the computer system, sending a message from the first processor to a second processor that includes the third location, the second processor included in a plurality of processors included in the computer system, wherein the plurality of processors share the common memory, wherein each of the processors have a local memory, and wherein at least two of the processors are dislike and heterogeneous;

in response to receiving the message, retrieving, by the second processor, the instruction block from the third location;

storing data corresponding to the request in the local memory corresponding to the second processor;

in response to the signaling, reading by the second processor the second location from the instruction block;

in response to retrieving the instruction block from the third location, copying, by the second processor, the data from the first location in the common memory to the second processor's local memory;

<u>in response to retrieving the instruction block from the third location,</u> copying, by the second processor, the software code from the second location in the common memory to the second processor's local memory; and

processing the data by the second processor using the software code stored in the second processor's local memory.

2-47. (Canceled)